Call for participation:
The 14th IEEE/VDAT VLSI Design And Test Symposium (VDAT2010) is scheduled during July 7-9, 2010. The venue will be Chitkara Institute of Engineering & Technology (near Chandigarh).

Paper Submissions: Authors are invited to submit unpublished work in a maximum of 10 A4 size pages including figures, appendices and references. The submissions will undergo double-blind reviews: authors must not indicate their names or affiliations anywhere in the text or in the references. The submissions should represent original contributions and should not have been submitted to other forums, printed journals or conferences. Reusing material from published work, even from the author’s previously published work, is tantamount to plagiarism and is strongly discouraged. Please indicate the area clearly when you make the submission. The area must also be indicated in the first page of the manuscript, below the title. Please do not submit survey papers.

Tutorial Submissions: Authors may also submit proposals for half-day or full-day tutorials in the areas mentioned above. Please place the text “Tutorial Submission” below the title and provide details of the speaker(s) and duration of the tutorial. Tutorial submission must include an extended abstract.

Topics: VDAT welcomes paper submissions in all areas of VLSI Design and Embedded Systems. Representative areas:

- Area A - VLSI and Embedded Systems for Medical Applications and Healthcare
- Area B - VLSI and Embedded Systems for Energy Conversion/Battery Management
- Area C - VLSI and Embedded Systems for Communications and Computing
- Area D - VLSI and Embedded Systems for Other Applications, including Rural
- Area E – System Integration and Test
- Area F – VLSI Chip Design and Test

Subject area categories

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Fellowships are available for Indian postgraduate students and faculty working in the areas related to VLSI. The students who apply for fellowship must be students at the time of the symposium. Fellowship entitles you to a lower registration fee. No other support will be available. Hostel stay is possible for participants on a self-payment basis. Application details would be announced on VDAT website.

Final Submission of accepted papers: Microsoft's CMT (Conference Management Service) website: https://cmt.research.microsoft.com/VDAT2010/Default.aspx. The CMT website is now open. Please visit VDAT website http://vlsi-india.org/events/VDAT2010/ for guidelines for online submission. All submissions should be in PDF, max. 5MB size. Authors' identities should not be revealed to aid blind review.

Guidelines to authors provided at http://vlsi-india.org/docs/i2a-doc.html should be followed for initial paper submission. It is assumed that at least one author will register and attend the conference to present the paper.

Important dates:

- Last Date for Submission – February 15, 2010 (Papers, 1-hr Embedded tutorials, and Full-day/half-day tutorials)
- Last Date for Intimation of Acceptance – April 15, 2010
- Last Date for Authors to confirm their participation – April 20, 2010
- Last Date for Camera-ready of accepted papers – June 1, 2010
- Last Date for Presentation foils of accepted papers – June 7, 2010

Submission guidelines:

- Proposals for full-day (6 hours’ duration) or half-day (3 hours’ duration) tutorials are also invited. Tutorial proposals should have (i) Tutorial title and short summary (in about 200 words), (ii) Name, affiliation and contact details of the co-coordinating presenter and all other co-presenters, (iii) Target audience and prerequisites along with beneficial impact of the tutorial on the intended participants (in about 75 words), (iv) detailed tutorial program, delineated sub-topic-wise description of the structure with time duration of each (in about 2000 words), (v) short biographical sketch of each presenter (in about 200 words for each presenter).
- Proposals for embedded tutorials (1 hour duration) are also invited. The proposals must include the full papers. Panel discussion proposals must be submitted together with details of the topics, their relevance and the conflicts they will address, together with names of panelists.

Submissions must become members of the vdat@yahoogroups.com mailing list, where updates on the symposium will be sent. Send any queries related to registration and local arrangements only to vdat@vlsi-india.org

Final Submission of accepted papers: Authors of accepted papers will be required to submit full papers in PDF, and the Presentation Foils of their talks in Power Point or PDF format. Final submissions (full papers and presentations) will be made available as Proceedings. Please visit guidelines to authors on homepage and http://vlsi-india.org/docs/i2a-doc.html for page formatting guidelines on Final Paper Submission.