

Are you planning to submit a paper to VDAT 2004?

Use this checklist before submitting ...

- If you are submitting an abstract, it should be at least 4 pages long in A4 or letter sized printing with 10 point fonts, single spacing, including figures/tables.
- If you are submitting a full paper, the paper should not be more than 10 pages in A4 or letter size paper, with 10 point fonts, single spacing, including figures/tables.
- Make sure the paper is not already published elsewhere.
- Make sure you have not submitted the same paper to another conference/journal.
- Do not submit student project theses directly as papers.
- Ensure that your paper is directly related to VLSI.
- Ensure that there is original contribution in the paper. Look through IEEE publications and do some Internet search to get this confidence.
- Highlight the original contribution.
- If you are proposing a new scheme without actually implementing, that's acceptable in the workshop – but do make sure that you have done the literature survey and the proposed scheme seems viable.
- Compare your work with what is already published in the literature.
- Make sure that you have a set of relevant references and that you have cited them in the text of the paper.
- Use MS-Word to typeset your manuscript. The final submission will always be in MS-Word format.
- Make sure that the drawings are clear.
- Do not identify yourself in the paper. The reviews will be blind.
- Make sure that there are no typographical errors and check for grammatical errors. Preferably, get your paper reviewed by a colleague.
- Do not email the paper. Submit it to the “Docman” system. If you have submitted a paper to VDAT last year or to VLSI Design conference, you already know the procedure. If not, visit <http://vlsi.ccrl.nj.nec.com> and “Check-in”. The “Check-in” link will also become available from <http://vlsi-india.net>
- Wish you all the best!

C.P. Ravikumar
General Chair