



VDAT 2011 - CALL FOR PARTICIPATION

15th VLSI Design And Test Symposium

To promote applications and research related to all aspects of VLSI in India

<http://vlsi-india.org/>

July 7-9, 2011

Venue: Pune, India

(Venue to be announced)

Theme: VLSI and Embedded System Solutions for Automotive Electronics

Conference committee

General Chair

C.P. Ravikumar

Texas Instruments India, Bangalore

Finance Chair

Dinesh Bapat

Texas Instruments India, Bangalore

Sponsored by VLSI Society of India



<http://vlsi-india.org/vsi>

In cooperation with



Supported by

Microsoft's Conference Management Service

Related Site: vlsiconference.com

Call for participation

The 15th VLSI Design And Test Symposium (VDAT 2011) is scheduled during the first week of July 2011 at Pune. The three-day event comprises full-day tutorials on the first day, and presentations of the accepted papers, keynote talks, invited talks and panel discussions on the subsequent two days.

Paper Submissions

Papers dealing with the design and testing of automotive electronics are especially solicited. A separate category called 'Automotive Electronics' will be available at the time of submission of papers.

Authors are invited to submit unpublished work in a maximum of 10 A4 size pages including figures, appendices and references. The submissions will undergo double-blind reviews: **authors must not indicate their names or affiliations anywhere in the text or in the references.** The submissions should represent original contributions and should not have been submitted to other forums, printed journals or conferences. Reusing material from published work, even from the author's previously published work, is tantamount to plagiarism and is strongly discouraged. Please indicate the area clearly when you make the submission. The subject area must also be indicated in the first page of the manuscript, below the title. Please do not submit survey papers.

Tutorial Submissions

Half-day and Full-day tutorials are invited in topics related to all aspects of automotive electronics.

Authors may also submit proposals for half-day or full-day tutorials in the areas mentioned above. Please place the text "Tutorial Submission" below the title and provide details of the speaker(s) and duration of the tutorial. Tutorial submission must include an extended abstract.

Download Tutorial Proposal Template format in PDF: <http://vlsi-india.org/docs/vdat-TutorialProposal.pdf>

Topics: VDAT welcomes paper submissions in all areas of VLSI Design and Embedded Systems. Representative areas:

- Area A** - VLSI and Embedded Systems for Medical Applications and Healthcare
- Area B** - VLSI and Embedded Systems for Energy Conversion/Battery Management
- Area C** - VLSI and Embedded Systems for Communications and Computing
- Area D** - VLSI and Embedded Systems for Other Applications, including Rural
- Area E** - System Integration and Test
- Area F** - VLSI Chip Design and Test

VDAT paper submission will be through Microsoft's CMT:

<https://cmt.research.microsoft.com/VDAT2011/Default.aspx>

Please visit VDAT website <http://vlsi-india.org/> for updates.

All submissions should be in PDF, with a maximum 10 pages and 5MB size limit. All coauthors names and contacts should be entered at the submission site while uploading the file.

For papers, author/s identities should not be revealed in any form in the PDF to aid blind review. Submissions not adhering to this will not be considered for review. Only the Tutorials submission must include speakers name and details.

Guidelines to authors provided at <http://vlsi-india.org/docs/i2a-doc.html> should be generally followed for initial paper submission.

It is assumed that at least one author will register and attend the conference to present the paper.

Other Subject area categories

Alternate Energy	Computer applications in Medicine	Medical imaging	Solar/PV Inverters
Battery Management	Digital X-Ray	Medical instrumentation and Measurements	System integration and Test
Biomechanics and Rehabilitation Engineering	Energy Conversion	Power Management	Telehealth
Biomedical Applications	Energy Harvesting	Precision Agriculture	Telemedicine
Biomedical Electronics	Environment Preservation	Rural Applications	Ultrasound
Biomedical Signal Processing	Medical Electronics Solutions	Rural Communication	VLSI Chip Design & Test
Communications and Computing	Medical image Compression	Smart Phones	

Important dates

Last Date for Submission – February 15, 2011 (Papers, 1-hr Embedded tutorials, and Full-day/half-day tutorials)

Last Date for Intimation of Acceptance – April 15, 2011

Last Date for Authors to confirm their participation – April 20, 2011

Last Date for Camera-ready of accepted papers – June 1, 2011

Last Date for Presentation foils of accepted papers – June 7, 2011

Exhibits

Exhibits of electronic solutions for the automotive industry are welcome. Please write to vdat-sponsor@vlsi-india.org for details

Fellowship

For students, faculty members, and employees of government R&D organizations, a lower registration fees will be offered in the form of fellowship. The students who apply for fellowship must be students at the time of the symposium. Fellowship entitles you to a lower registration fee. No other support will be available.

VDAT History

Event Title	Venue	Date	Participants
1 st VDAT	Chennai	January 7, 1998	30
2 nd VDAT	New Delhi	August 6-7, 1998	70
3 rd VDAT	New Delhi	August 20-21, 1999	120
4 th VDAT	New Delhi	August 25-26, 2000	150
5 th VDAT	Bangalore	August 16-18, 2001	220
6 th VDAT	Bangalore	August 29-31, 2002	300
7 th VDAT	Bangalore	August 28-30, 2003	300
8 th VDAT	Mysore	August 26-28, 2004	250
9 th VDAT	Bangalore	August 10-13, 2005	350
10 th VDAT	Goa	August 9-12, 2006	250
11 th VDAT	Kolkata	August 8-11, 2007	250
12 th VDAT	Bangalore	July 23-26, 2008	200
13 th VDAT	Bangalore	July 8-10, 2009	200
14 th VDAT	Himachal Pradesh	July 7-9, 2010	200