



VDAT 2008 - CALL FOR PARTICIPATION
12th IEEE VLSI Design And Test Symposium
July 23-26, 2008
 Wipro Campus, Electronics City, Hosur Road, Bangalore
Industry Sponsors



Texas Instruments India, Wipro Technologies
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<http://vlsi-india.org/vsi>

The 12th IEEE VLSI Design And Test Symposium (VDAT2008) is scheduled during July 23-26, 2008 at the Wipro Campus, Electronics City, Bangalore. Technical submissions on topics related to VLSI design, VLSI Testing, Formal Verification, and CAD Tools are invited.

Full-day Tutorials will be held on July 23, 2008. Embedded tutorials, invited talks, and Panel discussions on topics related to VLSI education will be held on July 24, 2008. July 25 and 26 will feature technical presentation of accepted papers, keynote talks, invited talks and panel discussions.

Visit <http://vlsi-india.org/events/vdat2008/> for more details.

Technical Submissions

The following types of submissions are invited:

- a) **Regular papers** – Authors are invited to submit unpublished work in a maximum of 10 A4 size pages including figures, appendices and references. The submissions will undergo double-blind reviews: **authors must not indicate their names or affiliations anywhere in the text** or in the references. The submissions should represent original contributions and should not have been submitted to other forums, printed journals or conferences. Reusing material from published work, even from the authors' own published work, is tantamount to plagiarism and is strongly discouraged. Authors of accepted papers will be required to submit full papers, and presentation foils of their talks in PowerPoint or PDF format. Final submissions (full papers or presentations) will be made available as Proceedings. Please visit the website which gives formatting guidelines to authors: <http://vlsi-india.org/docs/i2a-doc.html>.
- b) **Embedded Tutorials** - Proposals for embedded tutorials (1-hour) are invited. These are similar to regular papers in all respects, except that the author must include a brief biography at the end. Please indicate on the first page that the proposal is for an embedded tutorial. Submit the full paper to the conference website.
- c) **Special Session Proposals** – Proposals for special sessions, which may include invited talks/panels, must include complete details of the event and the biographies of speakers who are expected to be take part. All invited panelists/speakers are expected to register and attend VDAT.
- d) **Tutorial Proposals** - Proposals for full day (6-hour) or half-day (3-hour) tutorials are invited. Tutorial proposals should include (i) Tutorial title and short summary (ii) Name, affiliation, short biographies and contact details of all presenters, (iii) Target audience (iv) Bulleted list of topics that will be covered. Tutorial submissions must also be submitted to the conference website.

Online submission

On-line paper submission/review for VDAT Symposium is managed by Microsoft's CMT (Conference Management Service). Papers and proposals must be submitted to the conference website: <https://cmt.research.microsoft.com/VDAT2008/>. The website will be open from Feb 2008. All the submitting authors are notified on the status of acceptance via mail and VDAT website. Reviews for all submissions will be available through the CMT website. It is imperative that one of the authors will register and present the paper if the paper gets accepted. Regular papers get 20 minutes of presentation time. All accepted papers and embedded tutorials are included in the hardcopy/CD proceedings.

Fellowships

Fellowships are available for Indian postgraduate students and faculty working in the areas related to VLSI. A fellowship should not be viewed as an honor or recognition, but as an enabler to attend VDAT for students/faculty who do not get adequate support. Please do not apply if your organization can support you or you can support yourself. Fellowship entitles you to a lower registration fee. No other support will be available. The applicants for fellowship must be students/faculty at the time of the symposium. Application form will be made available at the VDAT2008 website. Forms must be forwarded to Fellowships Chair, **Partha Pratim Das**, by email vdat-fellow@vlsi-india.org no later than May 8, 2008.

Registration

Professionals/students interested in VLSI and related fields can register to attend VDAT Symposium. Registration details will be available by end of May 2008 at VDAT website. Fellowship recipients must register by paying the full amount under the fellow category tariff.

Symposium Committee

General Chair

C.P. Ravikumar, Texas Instruments India
 Bagmane Tech Park, CV Raman Nagar Bangalore-93
ravikumar@vlsi-india.org FAX: 91-80-25048213

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Sponsorships and Exhibits

Corporate sponsors and Exhibitors must contact the Sponsorship Chair for details no later than June 10, 2008.
N.S. Murty, NXP Semiconductors
vdat-sponsor@vlsi-india.org

Venue

The venue of the Symposium is the Learning Center on the Wipro campus, located in *Electronics City*. There are several hotels and service apartments for staying in Bangalore and information on these is available from the Internet. You will have to make advance bookings. Please look at a map of Bangalore before selecting a hotel.

To reach Electronics city, you must travel on Hosur road and enter the Phase I gate. Parking is available near Gate 6. The venue is about 20km from Railway station and ~15 km from the Airport. The weather in Bangalore during July is pleasant, with occasional showers.

Note: Prospective authors must visit the CMT Website, set up a login ID with profile at <https://cmt.research.microsoft.com/VDAT2008/> before making a submission. Help on using CMT is available at <https://cmt.research.microsoft.com/Cmt2Help/>. The website works best with Internet Explorer. Please ensure to enter the names of all co-authors at the time of submission. Changes to the title/list of authors are not permitted after the paper is accepted. Both PDF and DOC formats are allowed for initial submissions with a 5MB size limit.

Technical Program Committee

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Contact

- Program Chair vdat-pc@vlsi-india.org
 - Registration vdat-regn@vlsi-india.org
 - Local arrangements vdat-local@vlsi-india.org
 - Fellowship chair vdat-fellow@vlsi-india.org
 - Tutorial Chair vdat-tut@vlsi-india.org
 - General queries vdat@vlsi-india.org
 - Sponsorship/Exhibits vdat-sponsor@vlsi-india.org
- Please do not copy mails to all the accounts**

Important Dates

- Last Date for technical submission:** March 15, 2008
- Last Date for Sponsorship Proposal:** June 10, 2008
- Notification of acceptance:** April 25, 2008
- Last day to receive final manuscript:** May 10, 2008
- Fellowship application:** June 2, 2008
- Symposium Dates:** July 23-26, 2008



Event	Venue	Date	Participants
1 st VDAT	Chennai	January 7, 1998	30
2 nd VDAT	New Delhi	August 6-7, 1998	70
3 rd VDAT	New Delhi	August 20-21, 1999	120
4 th VDAT	New Delhi	August 25-26, 2000	150
5 th VDAT	Bangalore	August 16-18, 2001	220
6 th VDAT	Bangalore	August 29-31, 2002	300
7 th VDAT	Bangalore	August 28-30, 2003	300
8 th VDAT	Mysore	August 26-28, 2004	250
9 th VDAT	Bangalore	August 10-13, 2005	350
10 th VDAT	Goa	August 9-12, 2006	250
11 th VDAT	Kolkata	August 8-11, 2007	250