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To promote applications and research related to all aspects of VLSI in India

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VDAT 2010 - CALL FOR PARTICIPATION

14th IEEE/VSI VLSI Design And Test Symposium

<http://vlsi-india.org/>

July 7-9, 2010

Venue – Chitkara University Campus

Himachal Pradesh, India

<http://www.chitkara.edu.in/>



Call for participation:

The 14th IEEE/VSI VLSI Design And Test Symposium (VDAT2010) is scheduled during July 7-9, 2010. The venue will be Chitkara Institute of Engineering & Technology (near Chandigarh).

Paper Submissions: Authors are invited to submit unpublished work in a maximum of 10 A4 size pages including figures, appendices and references. The submissions will undergo double-blind reviews: **authors must not indicate their names or affiliations anywhere in the text or in the references.** The submissions should represent original contributions and should not have been submitted to other forums, printed journals or conferences. Reusing material from published work, even from the author's previously published work, is tantamount to plagiarism and is strongly discouraged. Please indicate the area clearly when you make the submission. The area must also be indicated in the first page of the manuscript, below the title. Please do not submit survey papers.

Tutorial Submissions: Authors may also submit proposals for half-day or full-day tutorials in the areas mentioned above. Please place the text “**Tutorial Submission**” below the title and provide details of the speaker(s) and duration of the tutorial. Tutorial submission must include an extended abstract.

Topics: VDAT welcomes paper submissions in all areas of VLSI Design and Embedded Systems. Representative areas:

- **Area A** - VLSI and Embedded Systems for Medical Applications and Healthcare
- **Area B** - VLSI and Embedded Systems for Energy Conversion/Battery Management
- **Area C** - VLSI and Embedded Systems for Communications and Computing
- **Area D** - VLSI and Embedded Systems for Other Applications, including Rural
- **Area E** – System Integration and Test
- **Area F** – VLSI Chip Design and Test

Subject area categories

Alternate Energy	Computer applications in Medicine	Medical imaging	Solar/PV Inverters
Battery Management	Digital X-Ray	Medical instrumentation and Measurements	System integration and Test
Biomechanics and Rehabilitation Engineering	Energy Conversion	Power Management	Telehealth
Biomedical Applications	Energy Harvesting	Precision Agriculture	Telemedicine
Biomedical Electronics	Environment Preservation	Rural Applications	Ultrasound
Biomedical Signal Processing	Medical Electronics Solutions	Rural Communication	VLSI Chip Design & Test
Communications and Computing	Medical image Compression	Smart Phones	

VDAT paper submission will be through Microsoft's CMT (Conference Management Service) website:

<https://cmt.research.microsoft.com/VDAT2010/Default.aspx> The CMT website is now open. Please visit VDAT website <http://vlsi-india.org/events/vdat2010/> for guidelines for online submission. All submissions should be in PDF, max. 5MB size. *Author's identities should not be revealed to aid blind review.*

Guidelines to authors provided at <http://vlsi-india.org/docs/i2a-doc.html> should be followed for initial paper submission.

It is assumed that at least one author will register and attend the conference to present the paper.

Important dates:

Last Date for Submission – February 15, 2010 (Papers, 1-hr Embedded tutorials, and Full-day/half-day tutorials)

Last Date for Intimation of Acceptance – April 15, 2010

Last Date for Authors to confirm their participation – April 20, 2010

Last Date for Camera-ready of accepted papers – June 1, 2010

Last Date for Presentation foils of accepted papers – June 7, 2010

Submission guidelines

Proposals for full-day (6 hours' duration) or **half-day** (3 hours' duration) **tutorials** are also invited. Tutorial proposals should have (i) Tutorial title and short summary (in about 200 words), (ii) Name, affiliation and contact details of the co-coordinating presenter and all other co-presenters, (iii) Target audience and prerequisites along with beneficial impact of the tutorial on the intended participants (in about 75 words), (iv) detailed tutorial program, delineated sub-topic-wise description of the structure with time duration of each (in about 2000 words), (v) short biographical sketch of each presenter (in about 200 words for each presenter).

Proposals for embedded tutorials (1 hour duration) are also invited. The proposals must include the full papers. **Panel discussion** proposals must be submitted together with details of the topics, their relevance and the conflicts they will address, together with names of panelists.

Submitting authors must become members of the vdat@yahoogroups.com mailing list, where updates on the symposium will be sent. Send any queries related to registration and local arrangements only to vdat@vlsi-india.org

Final Submission of accepted papers: Authors of accepted papers will be required to submit full papers in PDF, and the Presentation Foils of their talks in Power Point or PDF format. Final submissions (full papers and presentations) will be made available as Proceedings. **Please visit guidelines to authors on homepage** and <http://vlsi-india.org/docs/i2a-doc.html> for page formatting guidelines on Final Paper Submission.

VDAT History

Event Title	Venue	Date	Participants
1 st VDAT	Chennai	January 7, 1998	30
2 nd VDAT	New Delhi	August 6-7, 1998	70
3 rd VDAT	New Delhi	August 20-21, 1999	120
4 th VDAT	New Delhi	August 25-26, 2000	150
5 th VDAT	Bangalore	August 16-18, 2001	220
6 th VDAT	Bangalore	August 29-31, 2002	300
7 th VDAT	Bangalore	August 28-30, 2003	300
8 th VDAT	Mysore	August 26-28, 2004	250
9 th VDAT	Bangalore	August 10-13, 2005	350
10 th VDAT	Goa	August 9-12, 2006	250
11 th VDAT	Kolkata	August 8-11, 2007	250
12 th VDAT	Bangalore	July 23-26, 2008	200
13 th VDAT	Bangalore	July 8-10, 2009	200

Fellowships

Fellowships are available for Indian postgraduate students and faculty working in the areas related to VLSI. The students who apply for fellowship must be students at the time of the symposium. Fellowship entitles you to a lower registration fee. No other support will be available. Hostel stay is possible for participants on a self-payment basis. Application details would be announced on VDAT website.

Conference committee

General Chairs:

C.P. Ravikumar, Texas Instruments India, Bangalore

Mohit Chitkara, Chitkara University, Chandigarh

Finance Chair:

S.R.Gopal Naidu, Texas Instruments India, Bangalore

Local Organization Chair:

Rajnish Sharma, Chitkara University, Chandigarh

VDAT website will be updated with the details of Organizing Committee and Travel information.

Corporate Support for VDAT

Companies that wish to provide corporate support for VDAT may contact the Finance Chair (vsiaccounts@vlsi-india.org) or General Chairs (ravikumar@vlsi-india.org or chitkara1@gmail.com)

There will be provision for product exhibits.